BUS ARCHITECTURE AND
SHARED BUS ARBITRATION
METHOD FOR A COMMUNICATION
PROCESSOR, Rainer R. Hadwiger et al.
Docket No. A0312.70496US00

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FIG. 1
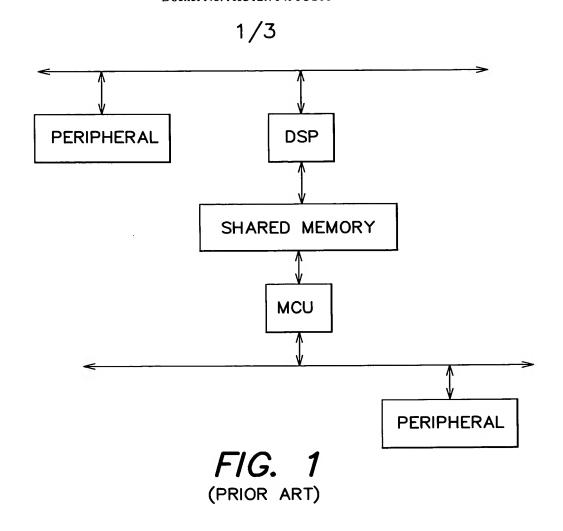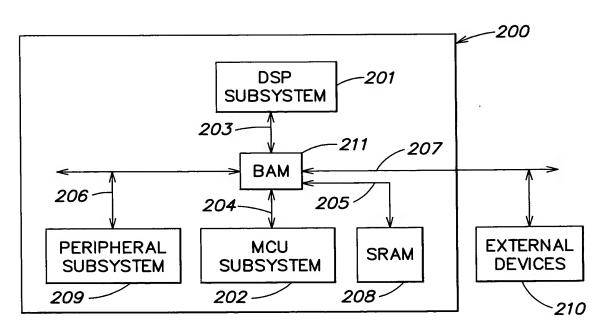(PRIOR ART)

FIG. 2

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FIG. 3

BUS ARCHITECTURE AND
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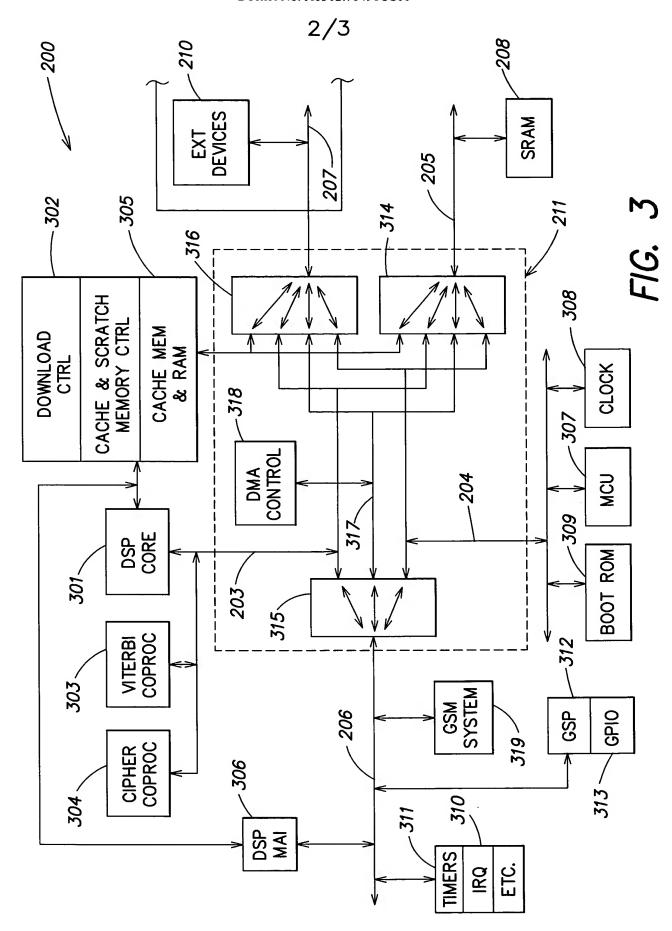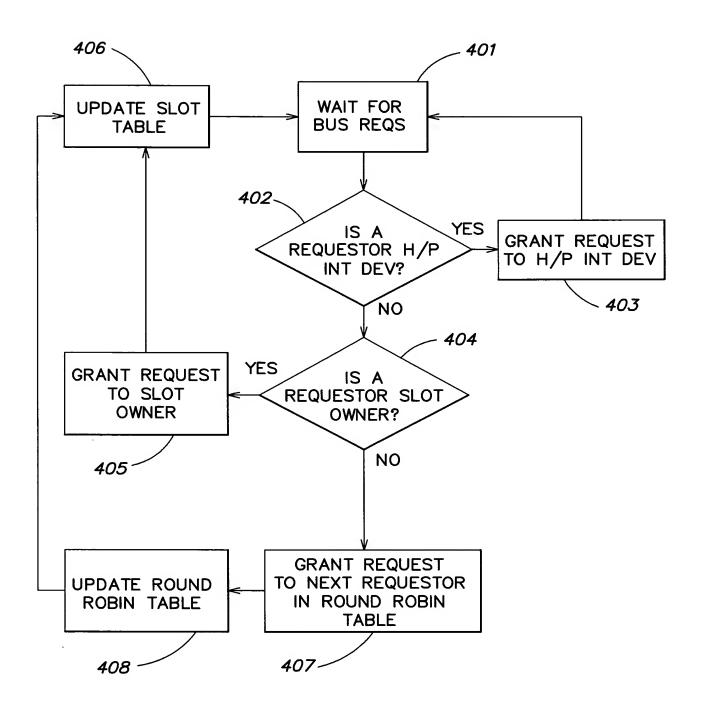PROCESSOR, Rainer R. Hadwiger et al.
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FIG. 4